

Amendments to the Claims:

Please amend claims 1, 15, and 29 and cancel claims 26 and 41. Following is a complete listing of the claims pending in the application, as amended:

1. (Currently Amended) A method in a computer system for reconfiguring a path between a source node and a destination node, the method comprising:
establishing a first path between the source node and the destination node, the path having a virtual address;
providing the virtual address to the source node for use in transmitting data from the source node to the destination node via the established path, wherein the virtual address and data are stored in a frame that includes a header and a payload; and
after providing the virtual address to the source node, establishing a second path between the source node and the destination node so that when the source node transmits data using the provided virtual address the data is transmitted via the second path rather than via the first path.
2. (Original) The method of claim 1 wherein the establishing of the second path is performed transparently to the source node.
3. (Original) The method of claim 1 wherein the path is established through a network of switches.
4. (Original) The method of claim 1 wherein the path is established through switches with ports and wherein the establishing of a path includes identifying a source-side port and a destination-side port for each switch.
5. (Original) The method of claim 4 wherein the establishing of the path includes providing the virtual address to each source-side port of a switch in the path.

6. (Original) The method of claim 5 wherein the virtual address of source-side port is used to map the source-side port to the destination-side port of the switch.

7. (Original) The method of claim 1 including identifying a virtual address for sending data from the source node to the destination node, the identified virtual address being provided to the source node.

8. (Original) The method of claim 7 wherein the identified virtual address is not currently used by any source-side ports of the switches.

9. (Original) The method of claim 7 wherein each port of each switch has a virtual address table for mapping virtual addresses to another port of the switch.

10. (Original) The method of claim 1 wherein when data is received at a port of a switch, the virtual address of the data is used to retrieve an indication of another port and the data is sent out of the switch through the other port.

11. (Original) The method of claim 1 wherein the establishing of path from the source node to the destination node includes identifying a source-side port and a destination-side port of each switch in the path.

12. (Original) The method of claim 1 wherein the data is a Fibre Channel frame.

13. (Original) The method of claim 1 wherein the switches are Fibre Channel compatible.

14. (Original) The method of claim 1 wherein the switches are interconnect fabric modules.

15. (Currently Amended) A computer system for reconfiguring a path between a source node and destination nodes, comprising:

a component that establishes a first path between the source node and a first destination node, the path having a virtual address, the first path being identified by a virtual address, so that when the source node transmits data using the virtual address, the data is transmitted via the first path; and

a component that stores the virtual address and data in a frame that includes a header and a payload; and

a component that, after establishing the first path, establishes a second path between the source node and a second destination node, the second path being identified by the virtual address so that when the source node transmits data using the provided virtual address after the second path is established, the data is transmitted via the second path.

16. (Original) The computer system of claim 15 including:

a component that provides the virtual address to a source node for use in transmitting data via the first path before the second path is established and via the second path after the second path is established.

17. (Original) The computer system of claim 15 wherein the establishing of the second path is performed transparently to the source node.

18. (Currently Amended) The computer system of claim 15 wherein the path is established through a network of switches.

19. (Original) The computer system of claim 15 wherein the paths are established through switches with ports and wherein the establishing of a path includes identifying a source-side port and a destination-side port for each switch in the path.

20. (Original) The computer system of claim 19 wherein the virtual address is used by source-side ports to map the source-side port to the destination-side port of the switch.

21. (Original) The computer system of claim 15 including:
a component that identifies a virtual address for sending data from the source node to a destination node, the identified virtual address being provided to the source node.

22. (Original) The computer system of claim 21 wherein the identified virtual address is not currently used by any source-side ports of the switches.

23. (Original) The computer system of claim 21 wherein each port of each switch has a virtual address table for mapping virtual addresses to another port of the switch.

24. (Original) The computer system of claim 15 wherein when data is received at a port of a switch, the virtual address of the data is used to retrieve an indication of another port and the data is sent out of the switch through the other port.

25. (Original) The computer system of claim 15 wherein the data is a Fibre Channel frame.

26. (Cancelled)

27. (Original) The computer system of claim 15 wherein the first destination node and the second destination node are different nodes.

28. (Original) The computer system of claim 15 wherein the first destination node and the second destination node are the same node.

29. (Currently Amended) A computer system for reconfiguring a path between a source node and a destination node, comprising:

means for establishing a first path between the source node and the destination node, the path having a virtual address; ~~and~~

means for establishing a second path between the source node and the destination node so that data transmitted using the virtual address is routed via the first path before the second path is established and via the second path after the second path is established; and;

means for storing the virtual address and data in a frame that includes a header and a payload.

30. (Original) The computer system of claim 29 including:

means for providing the virtual address to the source node for use in transmitting data to the destination node.

31. (Original) The computer system of claim 29 wherein the establishing of the second path is performed transparently to the source node.

32. (Original) The computer system of claim 29 wherein the path is established through a network of switches.

33. (Original) The computer system of claim 32 wherein the paths are established through switches with ports and wherein the means for establishing of a path includes identifying a source-side port and a destination-side port for each switch in the path.

34. (Original) The computer system of claim 33 wherein the virtual address is used by source-side ports to map the source-side port to the destination-side port of the switch.

35. (Original) The computer system of claim 32 wherein the switches are interconnect fabric modules.

36. (Original) The computer system of claim 29 including:
means for identifying a virtual address for sending data from the source node to the destination node and means for providing the virtual address to the source node.

37. (Original) The computer system of claim 36 wherein the identified virtual address is not currently used by any source-side ports of switches of the path.

38. (Original) The computer system of claim 36 wherein each port of each switch has a virtual address table for mapping virtual addresses to another port of the switch.

39. (Original) The computer system of claim 29 wherein the path comprises switches with ports and when data is received at a port of a switch, the virtual address of the data is used to retrieve an indication of another port and the data is sent out of the switch through the other port.

40. (Original) The computer system of claim 29 wherein the data is a Fibre Channel frame.

41. (Cancelled)